

FIG. 1

FIG. 2A

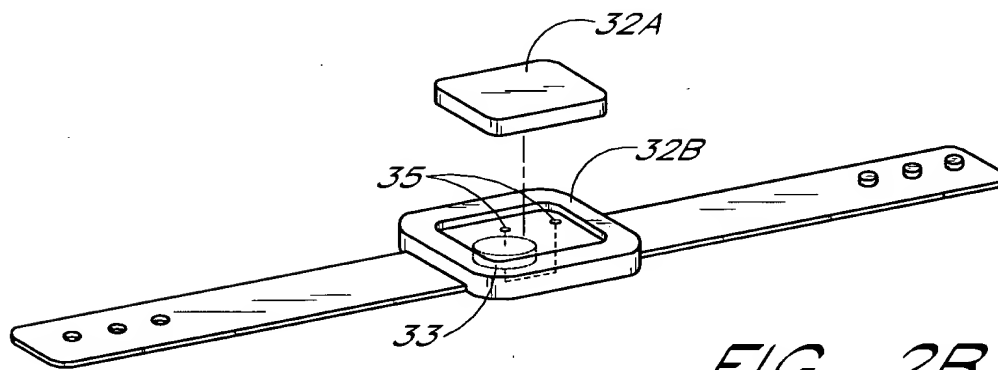
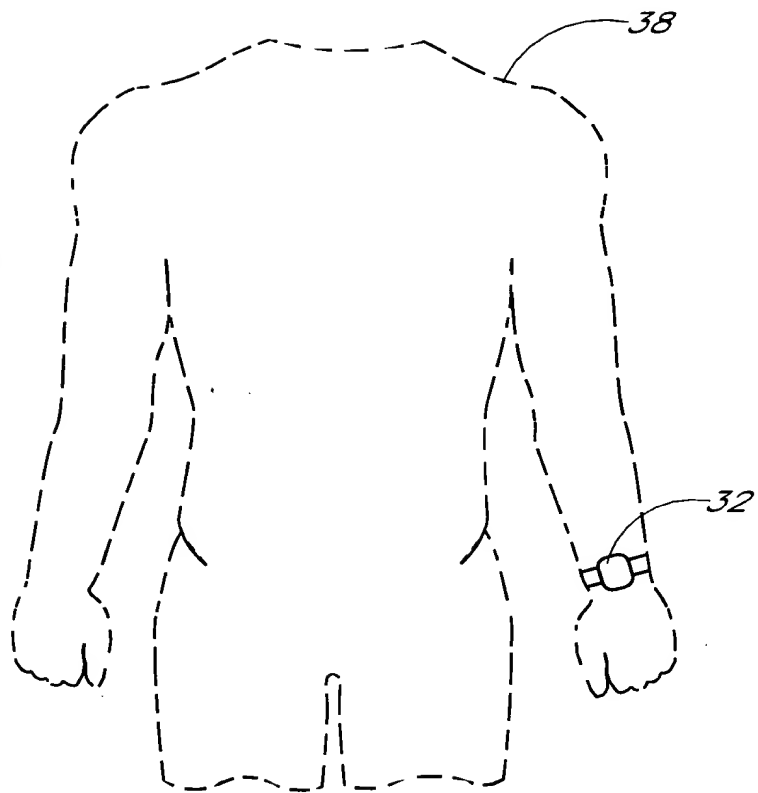


FIG. 2B

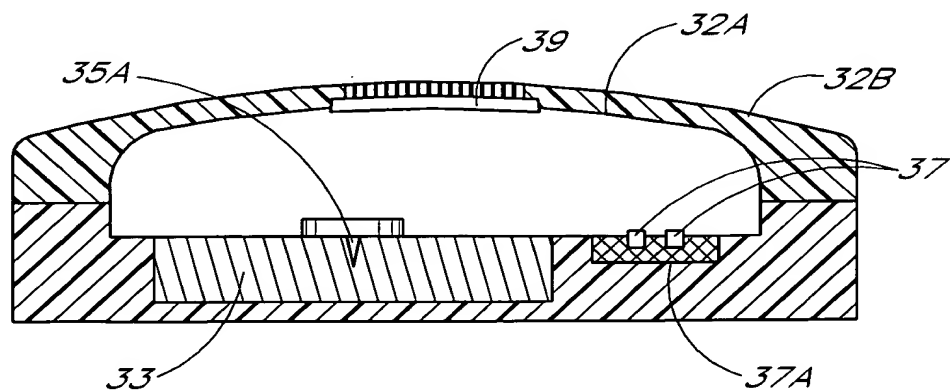


FIG. 2C

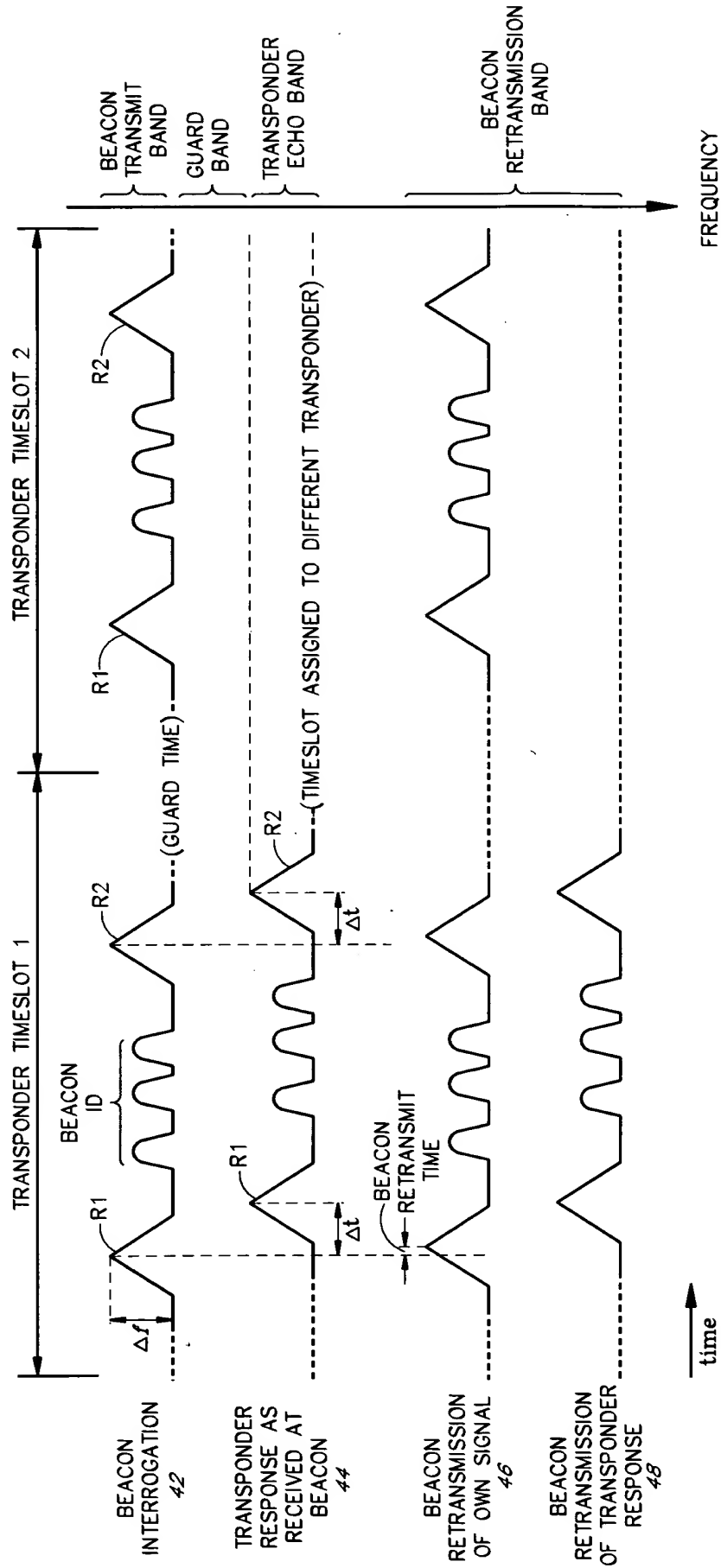


FIG. 3A

BEACON ID

1 0 0 1

BEACON INTERROGATION 42

TRANSPONDER RESPONSE AS RECEIVED AT BEACON 44

Δt

FIG. 3B.

U.S. Pat. 4,000,000

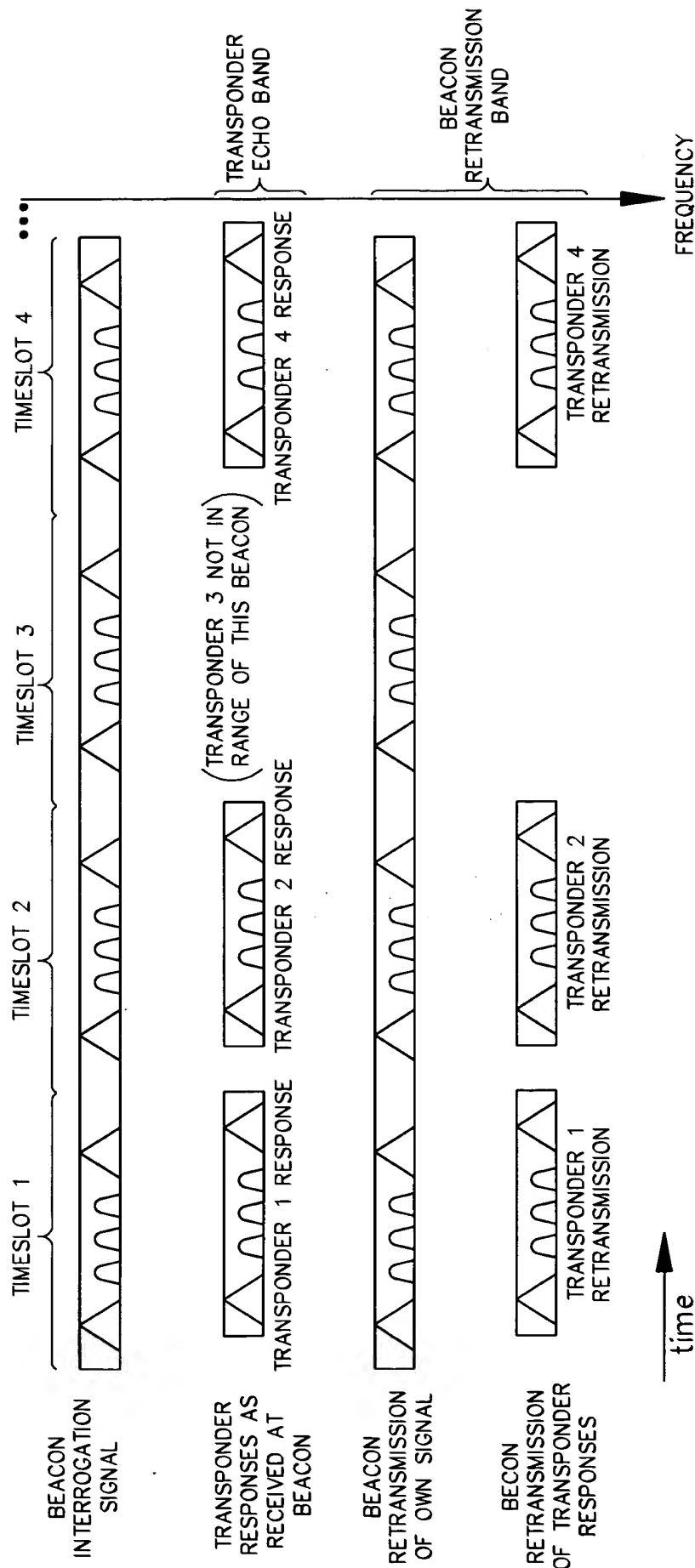


FIG. 4

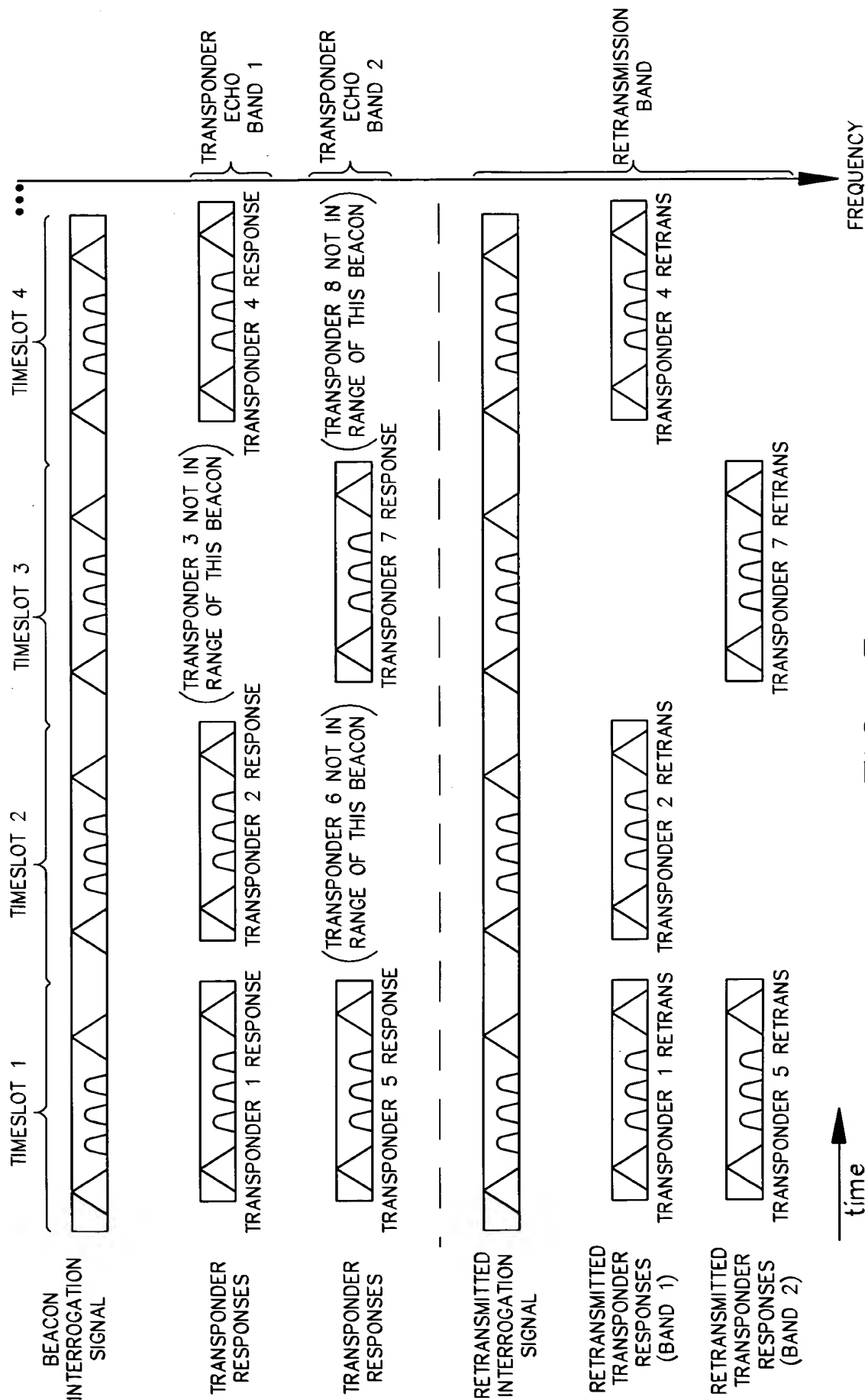


FIG. 5

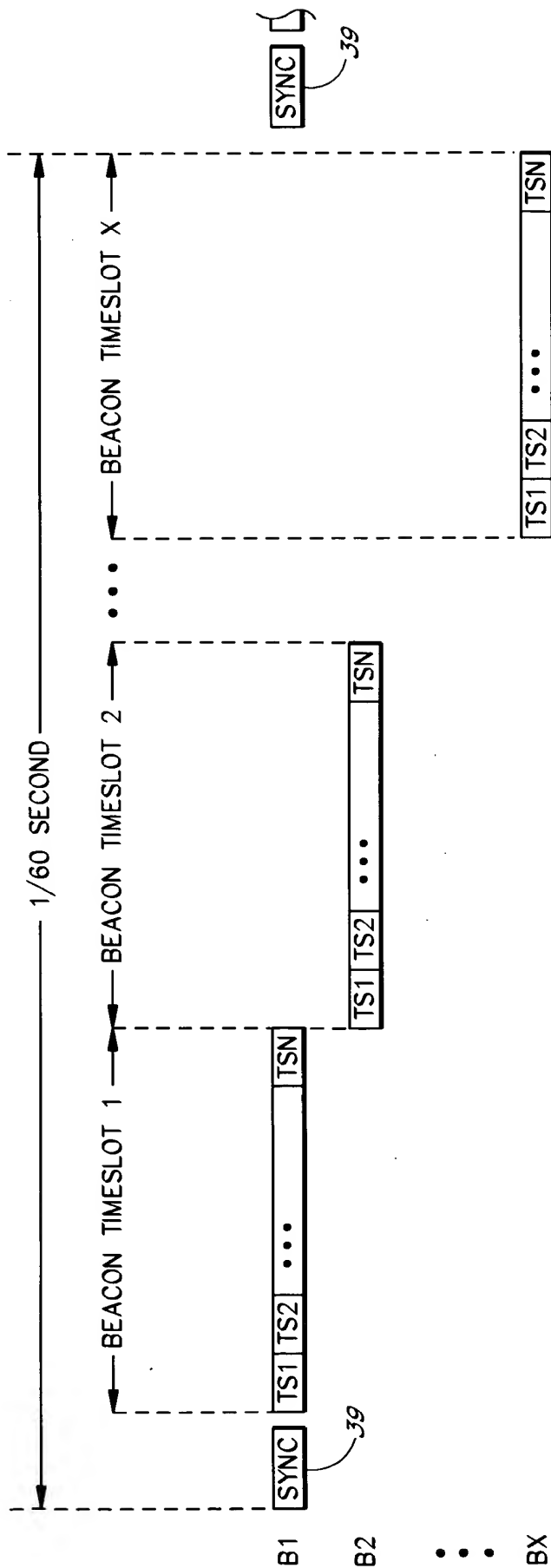


FIG. 6

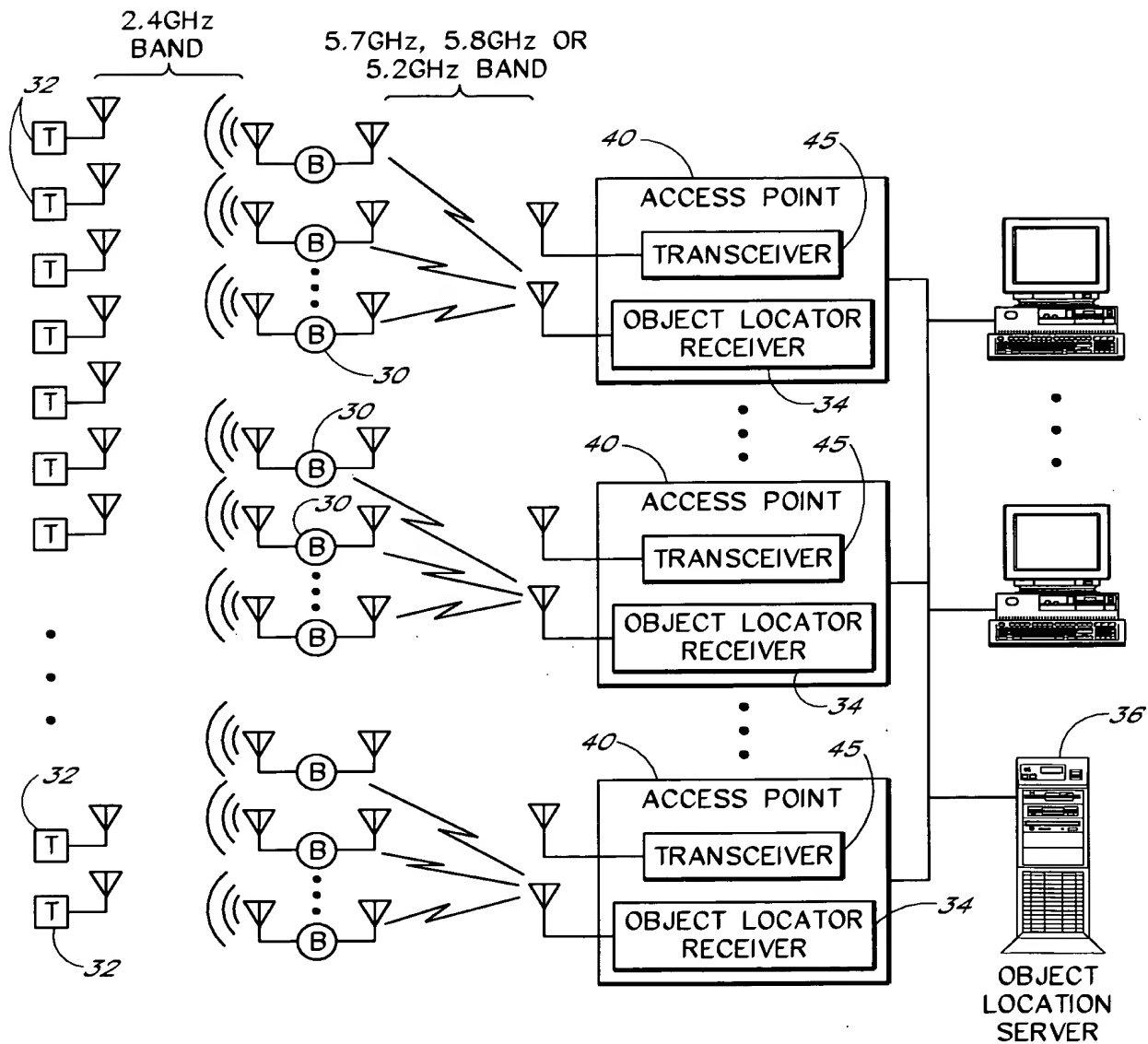


FIG. 7

FIG. 8

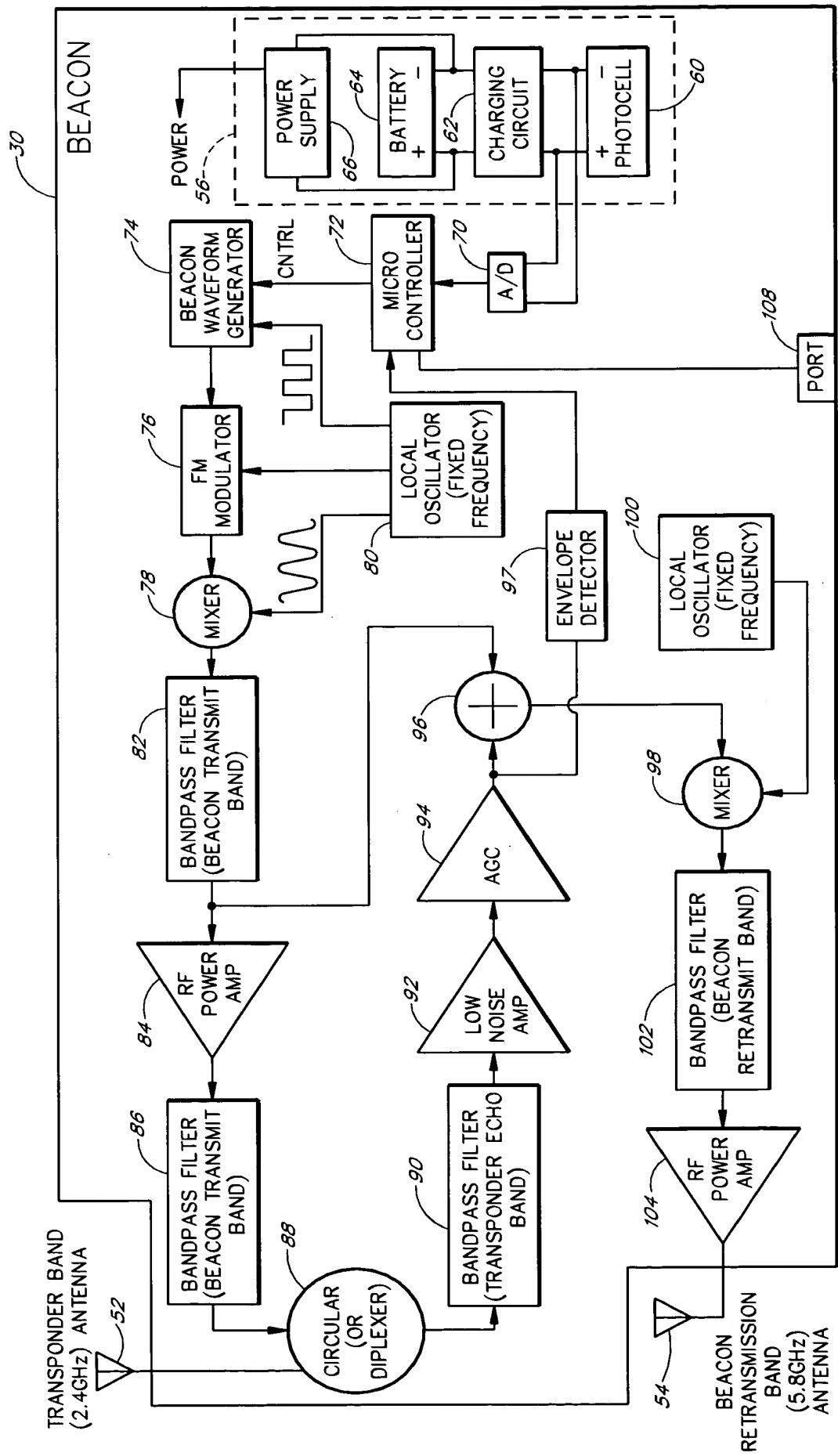


FIG. 8

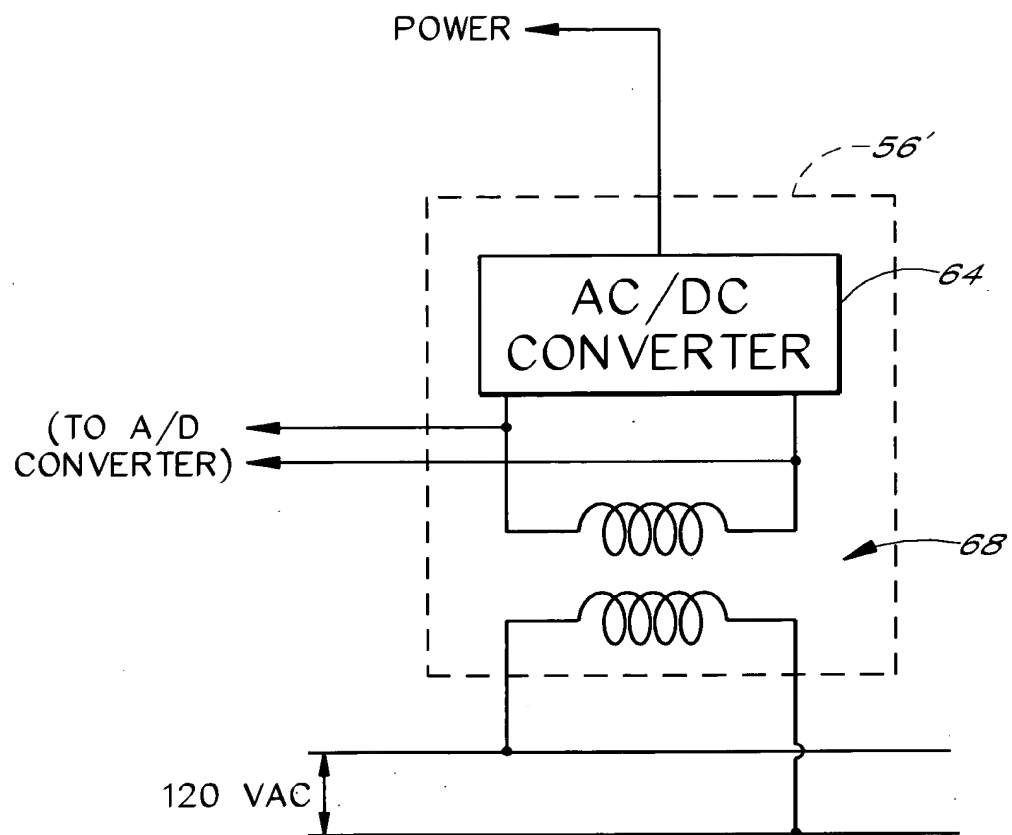


FIG. 9

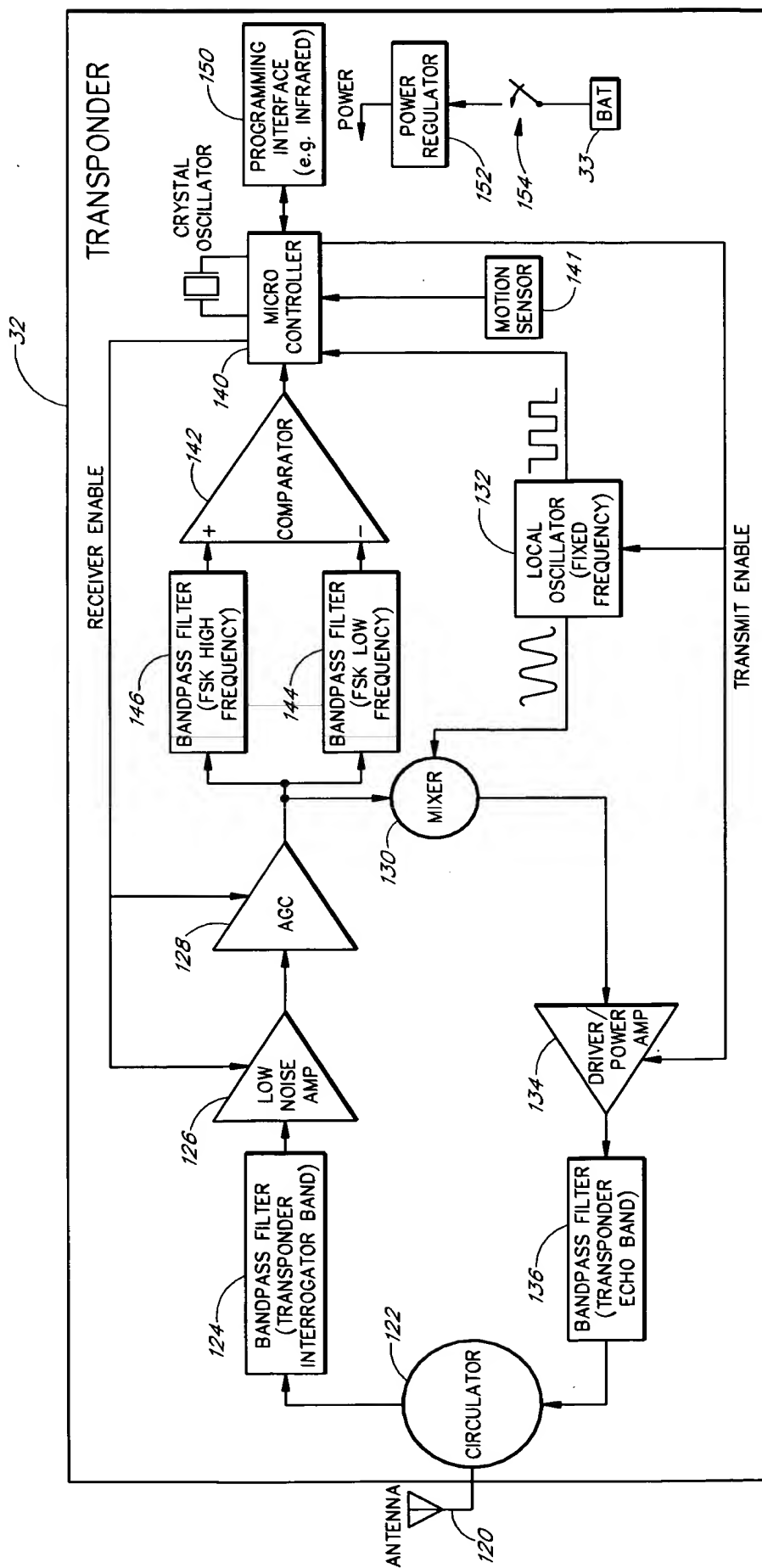


FIG. 10

FIG. 11 is a block diagram of a receiver system 34. The receiver system 34 includes a beacon retransmission band antenna 160, a bandpass filter (beacon retransmit band) 162, a low noise amp 164, a bandpass filter (upconverted beacon interrogate band) 166A, an AGC 168A, a mixer 170A, a lowpass filter 172A, an FM limiter/discriminator 174A, an A/D converter 176A, and a buffer memory 178A. The receiver system 34 also includes a digital signal processor 180, a host interface 182, a local oscillator (fixed frequency) 169A, a bandpass filter (upconverted transponder response band) 166B, an AGC 168B, a mixer 170B, a lowpass filter 172B, an FM limiter/discriminator 174B, an A/D converter 176B, and a buffer memory 178B. The receiver system 34 is connected to a host interface 182 via a digital signal processor 180.

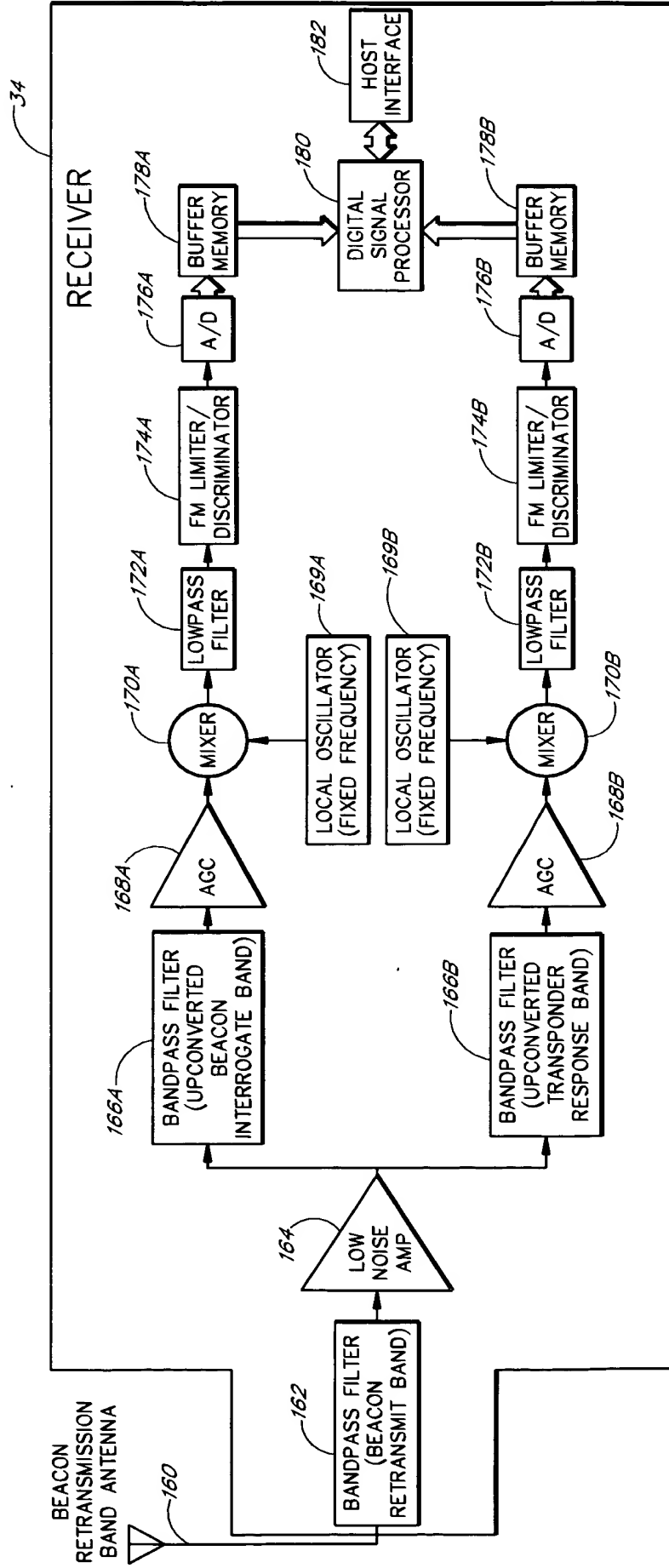


FIG. 11

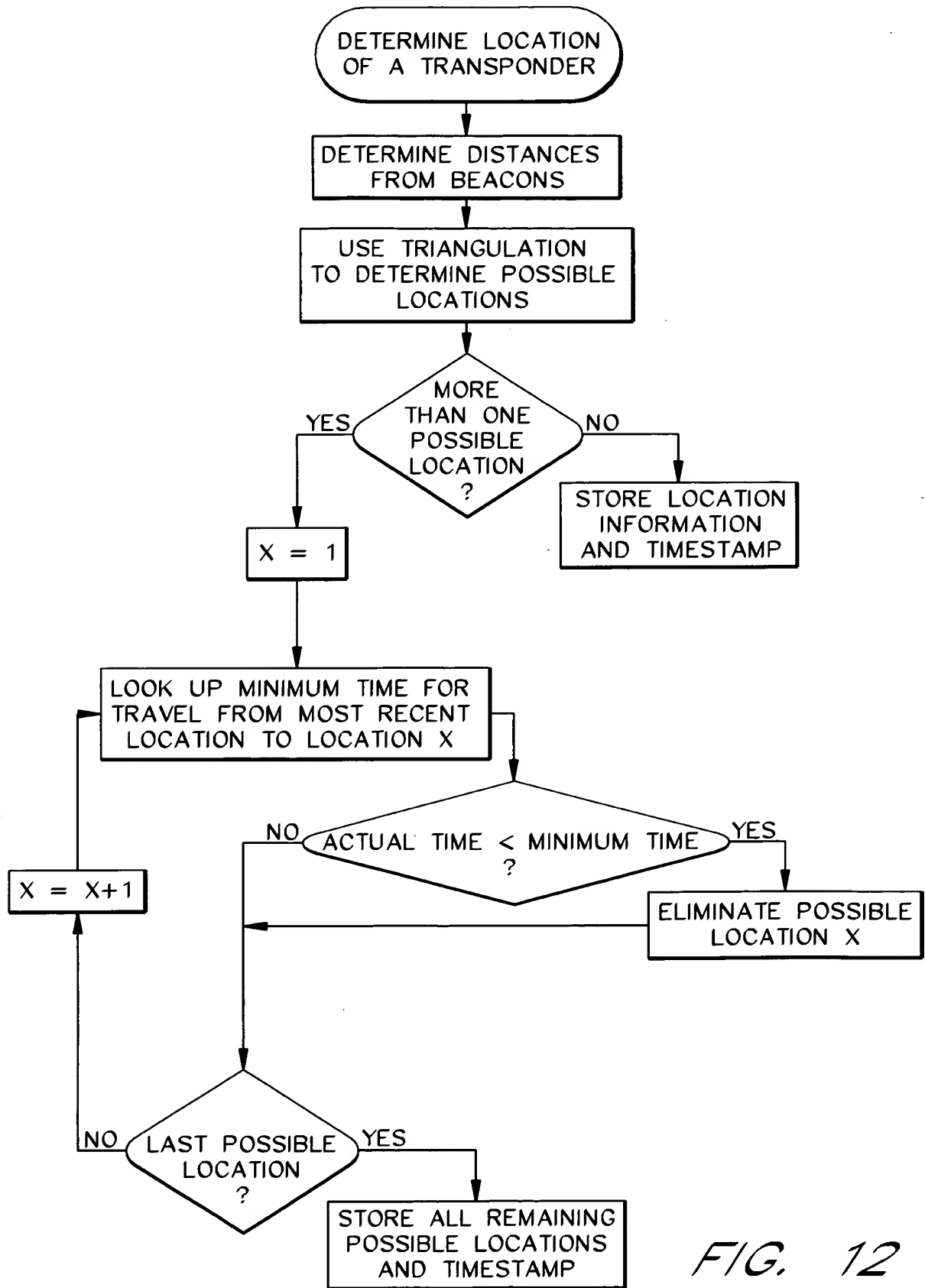


FIG. 12